

Class	Sub
ISSUE CLASSIFIC.	

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## U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED <i>LM2</i> Q.A. <i>SH</i>	

APPLICATION NO. 09/886166	CONT/PRIOR	CLASS 403	SUBCLASS 19	ART UNIT <del>2128</del> <del>2122</del> <del>2123</del> 2120	EXAMINER Phan
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## APPLICANTS

310

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Method for debugging of analog and mixed analog/digital circuits during simulation

PTO-2040  
12/89

## ISSUING CLASSIFICATION

ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent: No. _____ _____ _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>
Amount Due					Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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